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APPLICATION NO.	FILING DATE	FI	RST NAMED IN		ATTO	RNEY DOCKET NO.	CONFIRMATION NO.	
•	09/883,449 06/18/2001 Nai-Yin Sung E OF INVENTION: METHOD TO VERIFY THE PERFORMANCE OF BIST CIRCUIT					TS00-415	2688	
HILE OF INVENTION: N	METHOD TO VERIFY THE	PERFORMANCE C	F BIST CIRC	UIIRY FOR IESII	NG EMBEDDE	D MEMORY		
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06/22/2005 FFANAIA3 00000061 041679 09883449 EXAMINER ART UNIT CLASS-SUBCLASS								
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TRIMMINGS, JOHN P 2133 714-741000								
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Nai-Yin Sung et al.

Serial No.: 09/883,449

Examiner: John P. Trimmings

Group Art Unit: 2688 Filed: 06/18/2001

METHOD TO VERIFY THE PERFORMANCE OF BIST CIRCUITRY FOR TESTING For:

EMBEDDED MEMORY

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Dear Sir:

Transmitted herewith is the payment for the Issue Fee for the above-captioned patent application, which is due on 06/29/2005. Please charge the \$1,400 Issue fee to Deposit Account 04-1679.

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Dated: June 20, 2005

rk J. Marcélli

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Respectfully submitted,

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